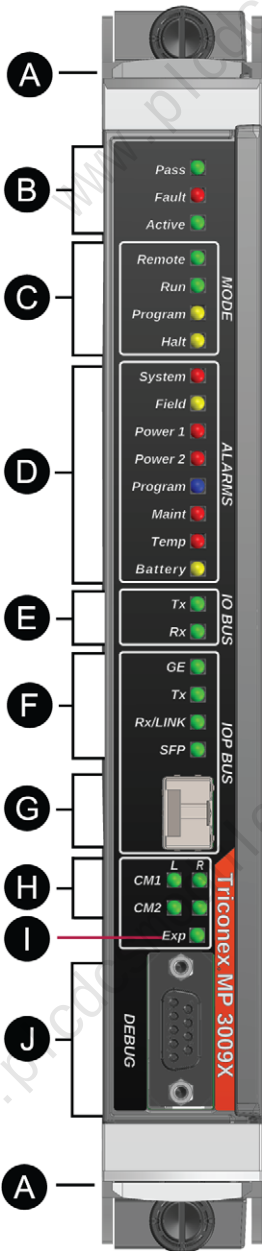


3009X Main Processor

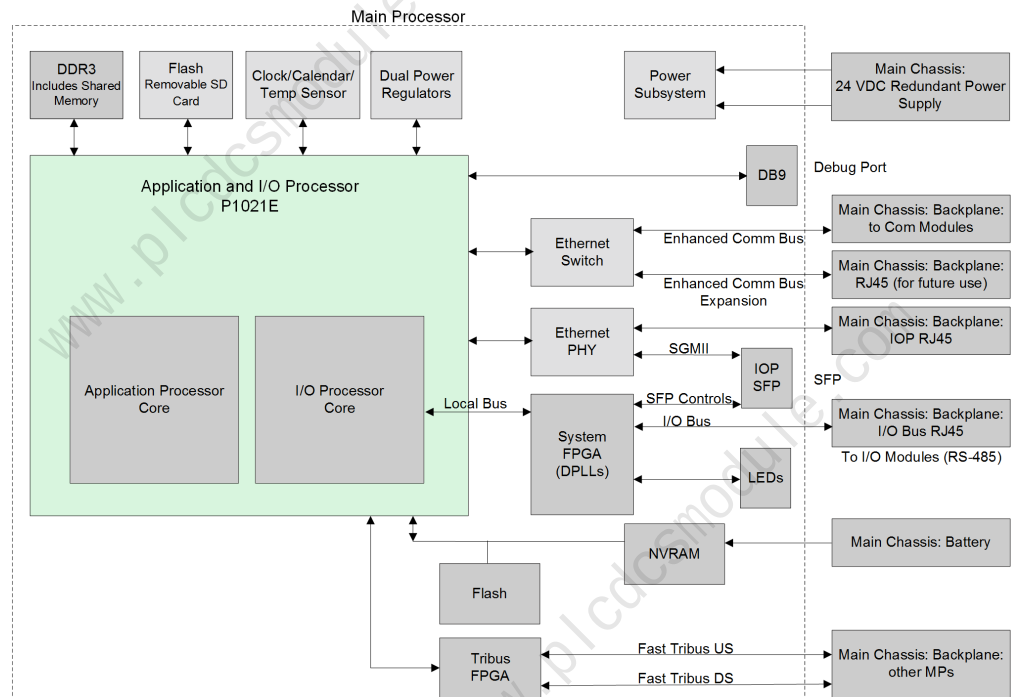
The Model 3009X Main Processor can be used in Model 8120X Main Chassis.

Figure 6 - Model 3009X Main Processor Front Panel



A	Module Ejectors	F	Input/Output Processor (IOP) Status
B	Module Status	G	Small Form-Factor Pluggable (SFP) Port for IOP Bus
C	Programming Modes	H	Enhanced Communication Bus Status
D	Alarms	I	For future use
E	Input/Output Bus Status	J	Debug Port for Schneider Electric use

3009X MP Architecture



3009X MP Specifications

Table 17 - 3009X Main Processor Specifications

Feature	Description
Main processor	QorIQ P1021, dual core processor, 32KB L1 cache, 256 KB L2 cache, 800 MHz
Memory	256 MB DRAM (without battery backup) 2 MB SRAM (with battery backup) 128 MB Flash PROM
Clock calendar	Time and date Battery backup Typical drift: ± 2 seconds/day Maximum drift: ± 2.16 seconds/day
Tribus	32-bit CRC-protected 32-bit DMA, fully isolated
Speed	1000 Mbps
Serial port	For diagread diagnostic analysis Optically isolated RS-232 interface on one 9-pin connector 500 VDC isolation
Expansion communication chassis interface:	
Protocol	Copper Ethernet, RJ-45 (on backplane)

Table 17 - 3009X Main Processor Specifications (Continued)

Feature	Description
Data rate	100 Mbps Ethernet
I/O interface:	
Serial protocol	RS-485
Serial data rate	375 Kbps
IOP bus	SFP, Ethernet, up to 43.5 miles (70 km)
Logic power	<15 Watts